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VLSI INTERVIEW QUESTION: Static Timing Analysis



Synopsis

First of all I would like to mention that this book can be used by Both Interviewer and Interviewee. If you are a student/interviewee - This book help you to crack the Interview or I would say, help you to perform far above your expectation. These questions are asked in several companies (Obviously not all questions in a single interview) by several experts. I am not claiming that "This Is It" but selection process depends on several other factors but I can assure you that if you have finished this book, you can Answer any questions in the interview. If you are a Interviewer - it will help you to extract more information in less time. It's not recommended to write down the questions which you would ask in the Interview but this book can help to understand what all different questions can be asked related to Static Timing Analysis. Structure of this book is different from any other books or you can say it's unique in one way. It will give you a feeling of real time scenario. Every question has it's level of Difficulty, Expected Answer , Explanation (Detail Overview), Follow up Questions and Objective. Last section "Follow up Question" is itself a unique concept in the publishing domain. It will guide you how different questions are linked with each other. Some time we prepare a lot but we don't know inter-related questions, it's obvious to miss few of them but when Interviewer asked those questions (during cross questioning), we realized that thinking that - Hope I have prepared this one also. :) In short this portion help you to prepare all the inter-related questions. As an author of this book, I have almost 10 year of experience and I know where people are lacking. My Goal is to bridge the Gap between the Academic Knowledge and the Industrial Requirement. I am not going to say a lot about my self. The purpose of writing this book is another step to bridge this gap. I hope, This book helps you to build your basics and also prepare you well in your Interview. At any point of time, if you feel that this book is not worth reading, please let me know. I will make sure that you will get your money back obviously the amount of time you have invested - I can't return that. But somehow - I believe you will like this. Note: This Book has 42 Main Questions and 119 Follow Up questions.

Book Information

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Customer Reviews

I found this book to be very useful for me. Definitely a worth buy. Before reading this, i had few illusions regarding timing concepts. Now i got an answer of how to look at those. Also this book written in a simple language, any one can easily understand. Follow up questions helps you to readily test your knowledge. Thanks a lot Puneeth for bringing this book up!

Hi Puneeth, Your book is very nice, it was in different approach for the interview attender, but as per my opinion this book is very much useful for the freshers level interview candidates it gives overall picture on STA. (i dont about this book for people who takes interviews it depends on their individual knowledge) or i can be completely wrong, because i had read your blog vlsi-expert completely from start to end and keep updating. so almost 90% question i was able to answer because of your blog knowledge before seeing the answers. but the follow up questions are very useful, got to know how the interview thinks and can pull in to our side. i can say its worth the money. Thank you for writing like these books, and keep writing. Thanks Teja.

This is one book which i usually refer at any time and there all your fundamentals will be back in your mind. It is a very simple, precise and the explanations will make you understand what the author wants you to know about timing. Its a worth buy as i found this book very useful during my interviews. Also i have been following the blog and 90% of the basics get cleared very easily. Thanks a lot Puneeth for writing these books !!!! Keep it up and Stay blessed. Thanks Tanvi

Very nice and detailed explanation of static timing analysis. Very good for aspirants in ASIC design and physical design people.

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